

29MA16 Pin-Node Correspondence

Pin			Node			Macrocell Fan-in
No.	Name	Functions(s)	No.	Name	Function(s)	
			1	GLOBAL	Global Preload	
			2	OBSERVE	Buried Register Observability	
1	CLK or LE	CLK or Latch Enable				
2	I[0]	Input				
3	IOF[0]	Dual Feedback I/O	3	RF[0]	Buried Feedback from IOF[0]	4
4	IOF[1]	Dual Feedback I/O	4	RF[1]	Buried Feedback from IOF[1]	4
5	IO[0]	Single Feedback I/O	5	R[0]	Buried Feedback from IO[0]	8
6	IO[1]	Single Feedback I/O	6	R[1]	Buried Feedback from IO[1]	12
7	IO[2]	Single Feedback I/O	7	R[2]	Buried Feedback from IO[2]	12
8	IO[3]	Single Feedback I/O	8	R[3]	Buried Feedback from IO[3]	8
9	IOF[2]	Dual Feedback I/O	9	RF[2]	Buried Feedback from IOF[2]	4
10	IOF[3]	Dual Feedback I/O	10	RF[3]	Buried Feedback from IOF[3]	4
11	I[4] or OE	Input or Output Enable				
12	GND					
13	I[1] or CLK	Input or CLK				
14	I[2]	Input				
15	IOF[4]	Dual Feedback I/O	11	RF[4]	Buried Feedback from IOF[4]	4
16	IOF[5]	Dual Feedback I/O	12	RF[5]	Buried Feedback from IOF[5]	4
17	IO[4]	Single Feedback I/O	13	R[4]	Buried Feedback from IO[4]	8
18	IO[5]	Single Feedback I/O	14	R[5]	Buried Feedback from IO[5]	12
19	IO[6]	Single Feedback I/O	15	R[6]	Buried Feedback from IO[6]	12
20	IO[7]	Single Feedback I/O	16	R[7]	Buried Feedback from IO[7]	8
21	IOF[6]	Dual Feedback I/O	17	RF[6]	Buried Feedback from IOF[6]	4
22	IOF[7]	Dual Feedback I/O	18	RF[7]	Buried Feedback from IOF[7]	4
23	I[3]	Input				
24	VCC					